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Practice & Experience using High Performance Computing & Quantum Computing to Speed-Up Data Science Methods in Scientific Applications

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(EuroHPC Joint Undertaking Governing Board Member for Iceland)

2022-05-26, IEEE MIPRO Conference, Opatija, Croatia



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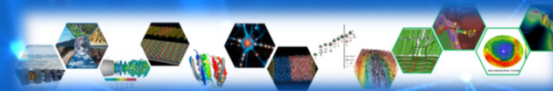
<https://www.youtube.com/channel/UCWC4VKHmL4NZgFfKoHtANKg>



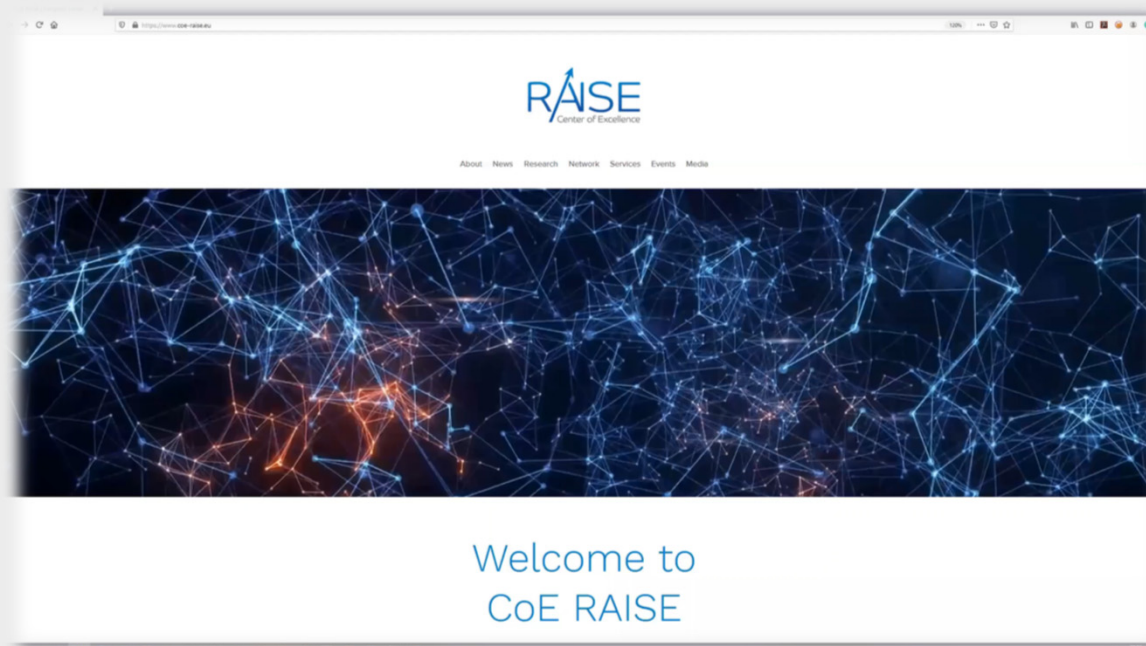
morris@hi.is



IHPC National Competence Center
(NCC) for HPC & AI in Iceland



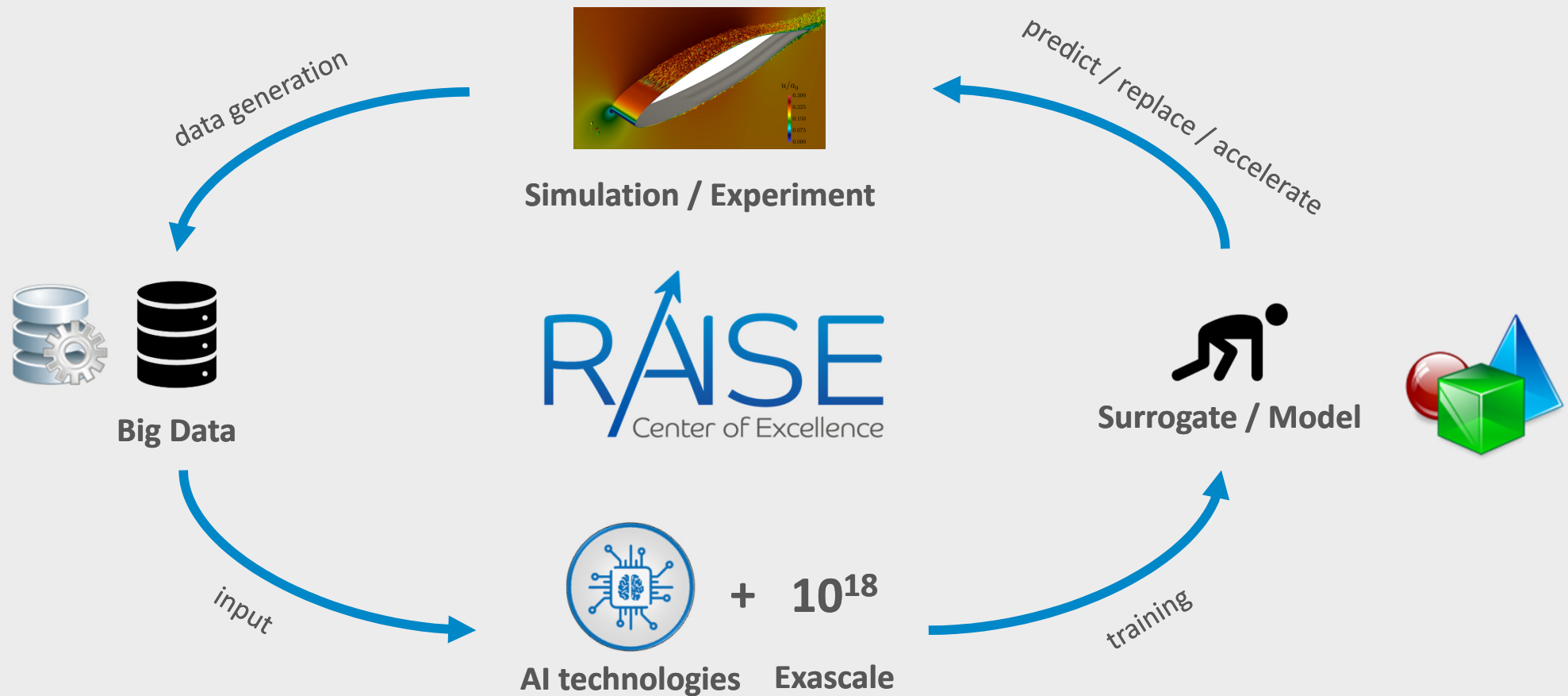
CoE RAISE Web Page & More Information



<https://www.coe-raise.eu>

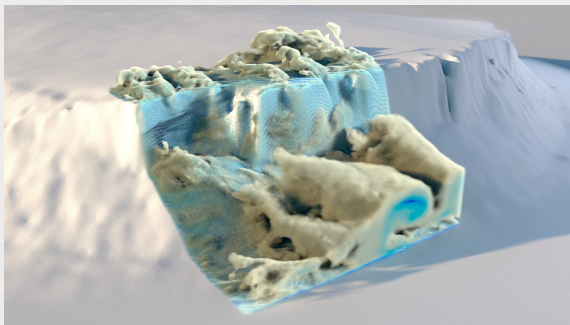
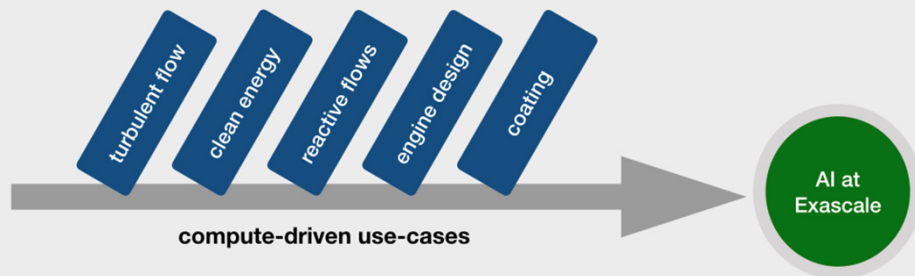


CoE RAISE – Motivation & Approach

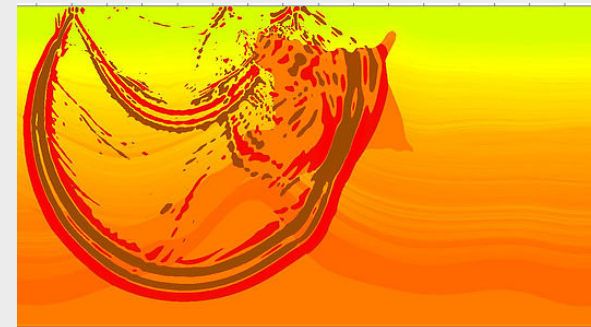
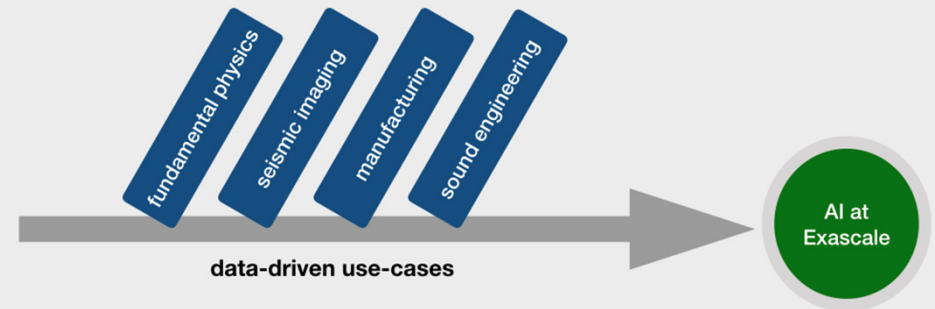


Use Cases in CoE RAISE

➤ Two kinds of use cases:



Example from use case "AI for wind farm layout": Turbulence generated by a cliff on Bolund Island, Denmark.

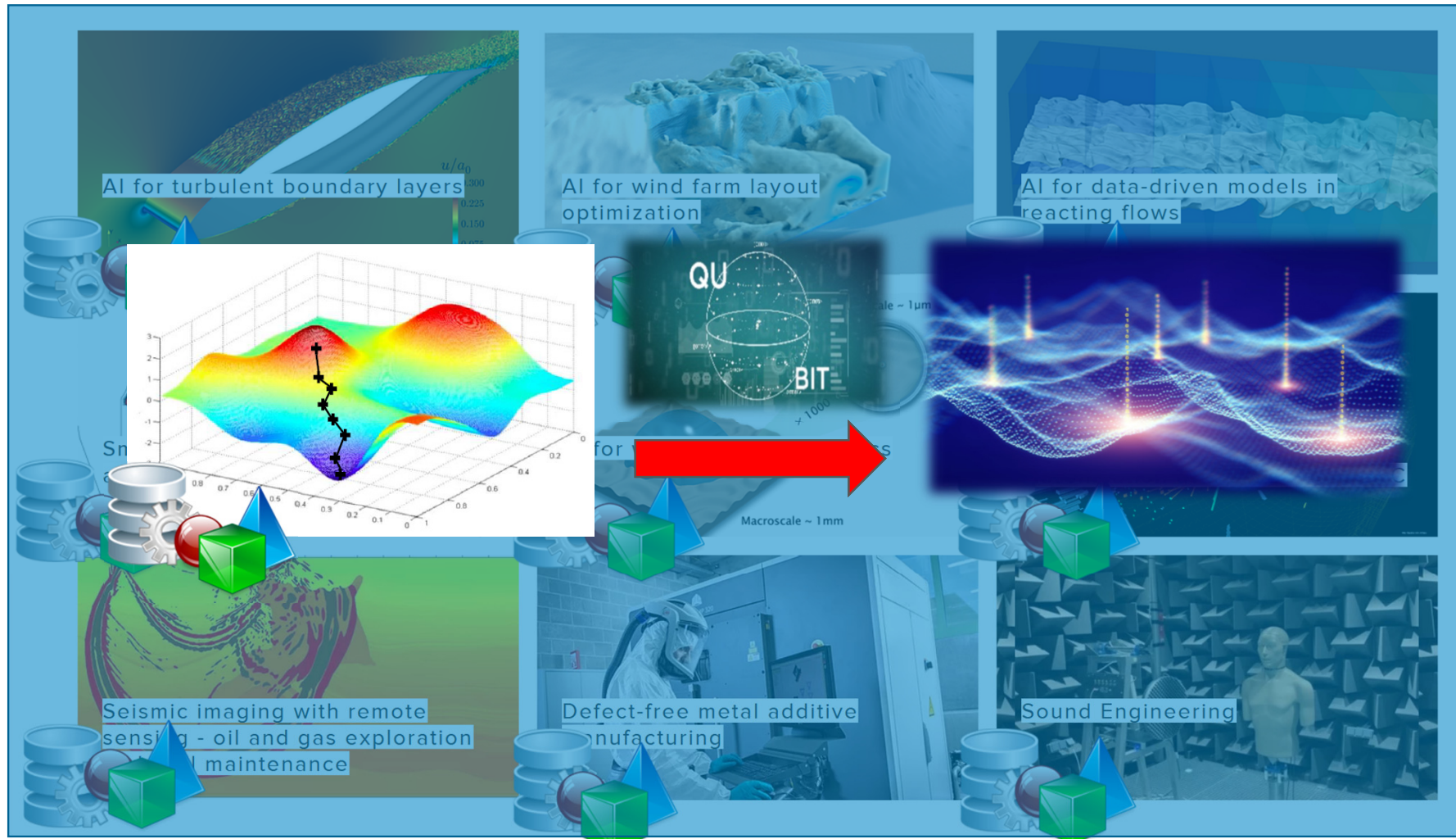


Example from use case "Seismic imaging with remote sensing - oil and gas exploration and well maintenance": Snapshot from a wavefield.

Compute- and Data-driven Use Cases – Data & Modeling

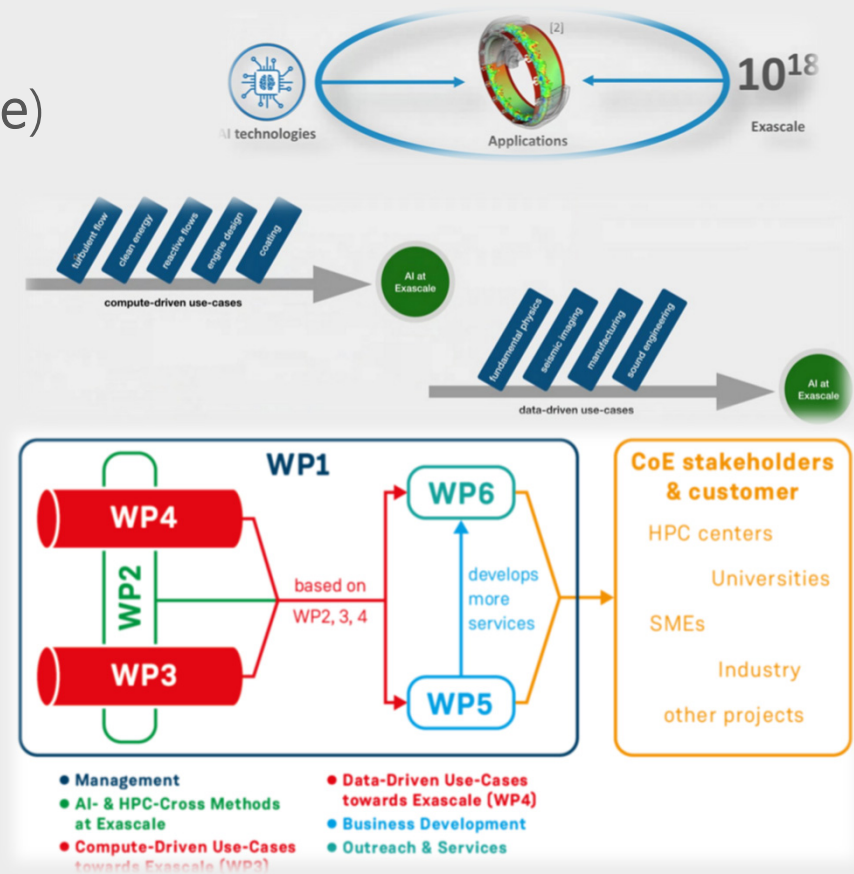


Example: Impact of Quantum Computing in Optimization

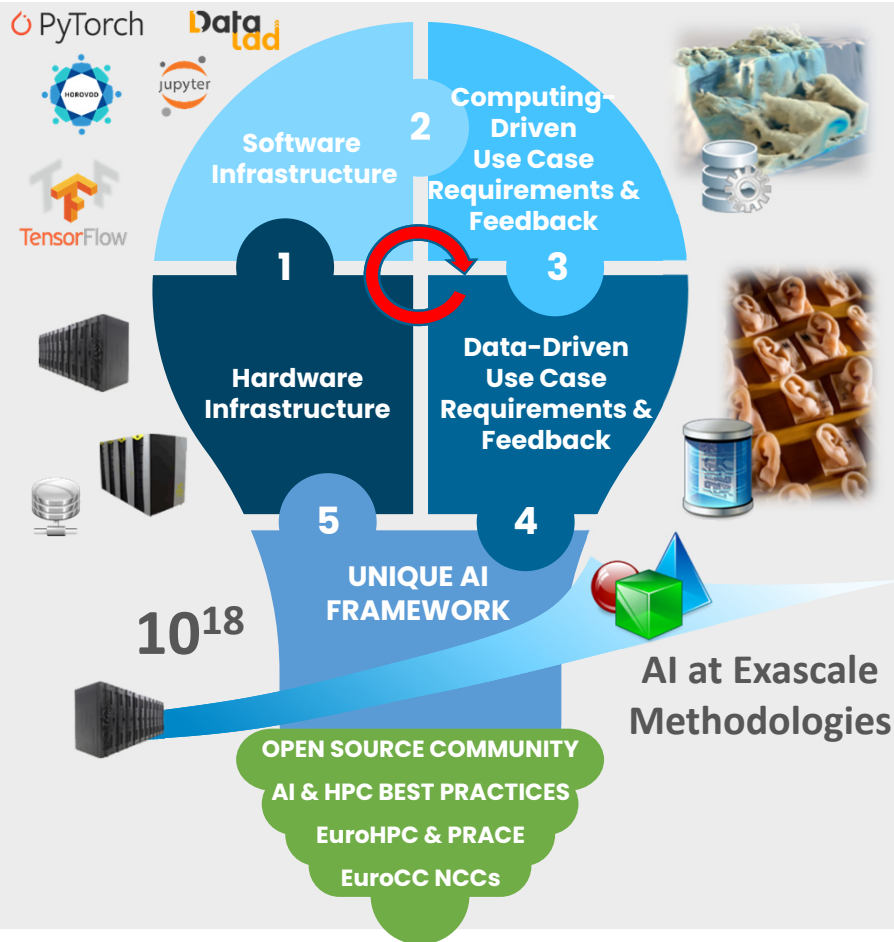


RAISE AI- & HPC-Cross Methods at Exascale in a nutshell

- WP3 (Compute-Driven Use-Cases towards Exascale)
- WP4 (Data-Driven Use-Cases towards Exascale)
- Developments in these WPs will be supported by the cross-linking activities of WP2
 - E.g. scaling machine & deep learning codes with frameworks like Horovod/Deepspeed
 - E.g. introduction to new AI methods such as Long-Short Term Memory (Time series)
 - E.g. data augmentation approaches
 - E.g. benchmarking HPC machines and offer also pre-trained AI algorithms (i.e., transfer learning)
 - E.g. offer neural architecture search methods for hyperparameter – tuning in semi-automatic way



Towards AI & HPC at Exascale with CoE RAISE Results



Hardware Infrastructure

Prepare & Document available production systems at partners' HPC centers

Examples: JUWELS (JUELICH), LUMI (UoICELAND), DEEP Modular Prototypes, JUNIQ (JUELICH), etc.

Software Infrastructure

Prepare & Document available open source tools & libraries for HPC & AI for implementing use cases

Examples: DeepSpeed and/or Horovod for interconnecting N GPUs for a scalable deep learning jobs

Computing-driven Use Cases Requirements & Feedback

Use cases with emphasize on computing bring in co-design information of AI framework & hardware

Examples: Feedback that sometimes TensorFlow does not work nicely, so use pyTorch in some cases

Data-driven Use Cases Requirements & Feedback

Use cases with emphasize on data bring in co-design information about AI framework & hardware

Examples: Deployment blueprint by using AI training on cluster module & inference/testing on booster

→ UNIQUE AI FRAMEWORK

Living design document & software framework blueprint for HPC & AI offering also pretrained AI models

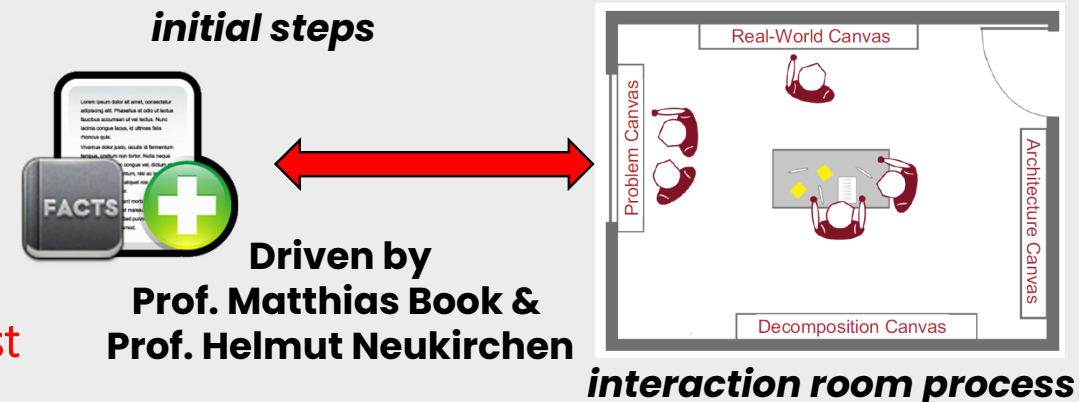
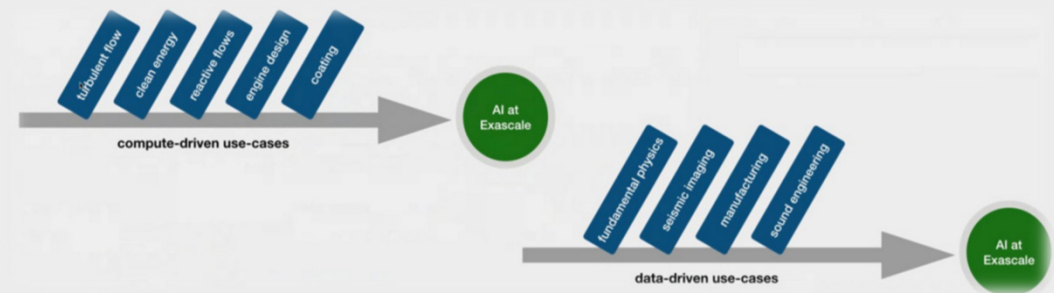
Selected Techniques to Identify Cross-Methods for HPC & AI

➤ Fact Sheets

- Foster initial understanding
- Living document & each Fact Sheet per WP3/WP4 Use Case
- *(Experience from many other EU projects)*

➤ Selected Contents

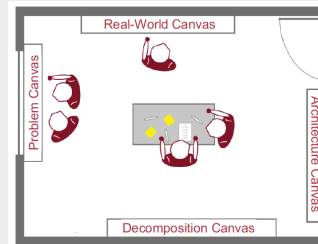
- Short Application Introduction
- Clarify Primary Contacts
- Codes/Libraries/Executables
- HPC System Usage Details
- Specific Platforms & 'where is what data'?
- **Machine/Deep Learning Approaches of Interest**



HPC Systems Engineering in the Interaction Room Seminar

➤ CoR RAISE Interaction Room Process as Next Step

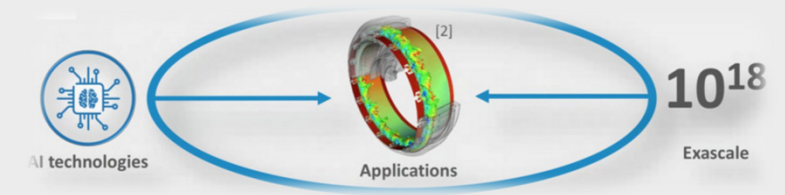
- Supports the proper software engineering design of the unique AI framework blueprint
- Expecting to work with WP3 & WP4 experts in an open minded way
- Process will be guided by Prof. Dr. Matthias Book (University of Iceland)
- Supported by Software Engineering & testing expert Prof. Dr. Helmut Neukirchen (University of Iceland)



HPC Systems Engineering in the Interaction Room

Matthias Book

with Morris Riedel, Jülich Supercomputing Centre / UoI and Helmut Neukirchen, University of Iceland




➤ CoE RAISE @ YouTube: <https://www.youtube.com/channel/UCAdIZ-v6cWwGdapwYxdN7dg>

➤ **Methology as one CoE RAISE outcome**

Book, M., Riedel, M., Neukirchen, H., Goetz, M.: **Facilitating Collaboration in High-Performance Computing Projects with an Interaction Room**, in conference proceedings of the 4th ACM SIGPLAN International Workshop on Software Engineering for Parallel Systems (SEPS 2017), October 22-27, 2017, Vancouver, Canada


RAISE Initial Identified AI/HPC Methods: Graph Example




BEFORE

Table 6: Use-case vs. AI-methods matrix.

Use-Case vs. AI-Methods	DA	NAS	AE	TL	PF	PIDL	LSTM
Turbulent boundary layers	X	X	X	X	X	X	
Wind farm layout optimization	X			X		X	
AI for data-driven models in reacting flows				X		X	
Smart models for next-generation aircraft engine design	X	X		X		X	
Wetting hydrodynamics		X	X			X	X
Event reconstruction and classification at the CERN HL-LHC		X		X			X
Seismic imaging with remote sensing - oil and gas exploration and well maintenance	X	X		X			
Defect-free metal additive manufacturing		X				X	X
Sound engineering	X	X		X			X



H2020-INFRAEDI-2018-2020



CoE RAISE

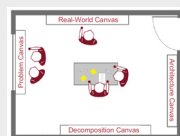
Center of Excellence "Research on AI- and Simulation-Based Engineering at Exascale"


Grant Agreement Number: 951733

MS2

AI/HPC Methods

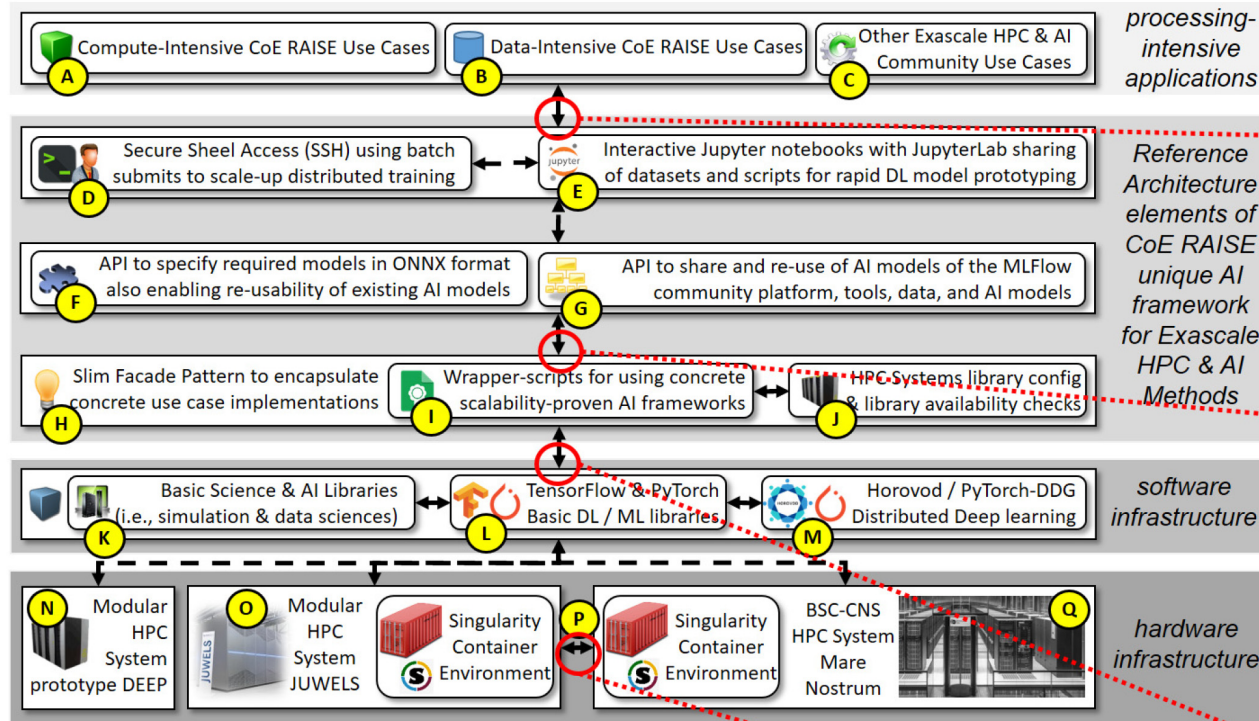
Draft





Use Case	AE	PIML	ANNs	CNN		NO	SMs			GNN	IN	LSTM	GRU
Details	CAE		RBF-ANN	U-Net	RESNET	FNO	AR	ARMA	ARIMA		JEDI-net		
AI for turbulent boundary layers	X	X											
AI for wind farm layout optimization			X				X	X	X				
AI for data-driven models in reacting flows				X						X			
Smart models for next generation aircraft engine design				X						X			
AI for wetting hydrodynamics						X							
Event reconstruction and classification at the CERN HL-LHC use case										X	X		
Seismic imaging with remote sensing for energy applications	X				X								
Detect-free metal additive manufacturing	X				X								
Sound Engineering												X	X

Practice & Experience: AI Software Framework Blueprint



Legend:



Tangible outputs of RAISE WP2 as part of the unique AI framework layout

- ✓ RQ6, RQ7, RQ8, RQ9
- ❖ Part of the framework layout plan is to provide containers in Singularity with prepackaged datasets & software stacks needed for AI agnostic to hardware & good I/O performance

processing-intensive applications

Reference Architecture elements of CoE RAISE unique AI framework for Exascale HPC & AI Methods

software infrastructure

hardware infrastructure

- ✓ RQ1, RQ2, RQ4, RQ5

- ❖ Parts of the framework layout plan is to provide Kernels for Jupyter notebooks with correct version setups of modules for specific HPC Systems

- ✓ RQ3, RQ6

- ❖ Parts of the framework layout plan is to provide a lightweight and abstract Python API building on ONNX enabling also exchanges via MLFlow/ClearML

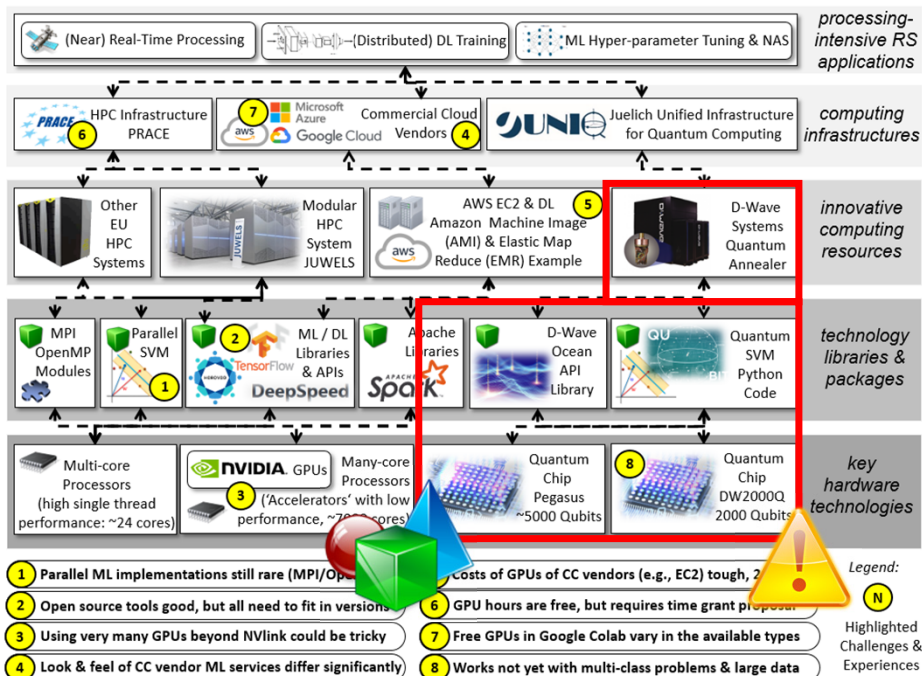
- ✓ RQ1, RQ2, RQ8, RQ9

- ❖ Parts of the framework layout plan is to provide a lightweight Python API that abstracts from low level versioning of AI packages (with proven scalability) and is harmonized with different available HPC system module versions

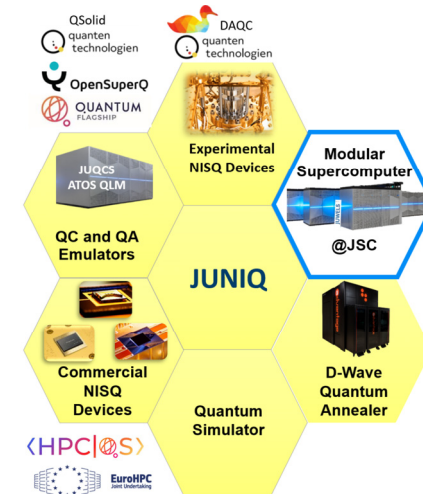
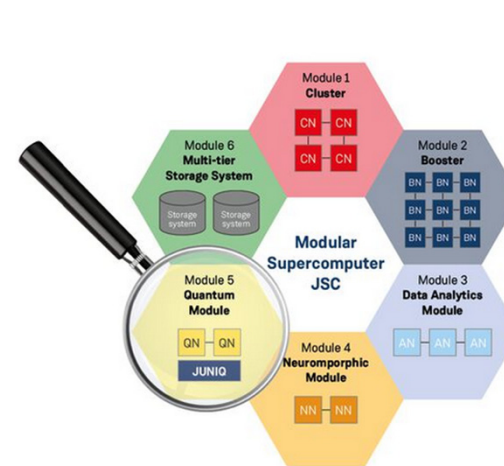


Continuously Updating: e.g. RayTune Hyperparameter Optimization Tool

Innovative Quantum Computing Hardware as 'Accelerators' RAISE Center of Excellence



Riedel, M., Cavallaro, G., Benediktsson, J.A.: Practice and Experience in using Parallel and Scalable Machine learning in Remote Sensing from HPC over Cloud to Quantum Computing, in conference proceedings of the IEEE IGARSS Conference, Brussels, Belgium, 2021, Physical and Online event, to appear <https://igarss2021.com/>



<https://www.fz-juelich.de/ias/jsc/EN/Expertise/JUNIQ/node.html>





drive. enable. innovate.



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