







WP2 Al- & HPC-Cross Methods at Exascale – Task 2.4: Software Design of a Unique Al Framework

Prof. Dr. – Ing. Morris Riedel et al. School of Engineering & Natural Sciences, University of Iceland 2021-11-24, RAISE All Hands Meeting, Online









@MorrisRiedel







WP2 Agenda & Tasks



Work package 2	13:15 – 14:25			
13:15 - 13:25	WP2 (UOI): Introduction AI- and HPC-Cross Methods	M. Riedel		
	at Exascale			
13:25 - 13:40	Task 2.1 (BSC): Modular and heterogeneous	G. Houzeaux		
	supercomputing architectures			
13:40 - 13:55	Task 2.2 (FZJ): Hardware prototypes	E. Inanc		
13:55 – 14:10	Task 2.4 (UOI): Software design of a unique AI	M.Riedel		
	framework			
14:10 - 14:25	Task 2.5 (UOI): Cross-Sectional AI Methods	M. Riedel		

WP2 Task 2.4



In this task, the software design of a unique use-case-driven AI framework is commenced within the frame of a planning and strategy layout phase. Therefore, the task contributors perform a use-case requirement analysis to decide on a generalized layout of the framework that includes all necessary components coming from the use-cases. It ensures that the developments in the use-cases are in line with a future integration into a generalized software framework by continuously monitoring them. A lively interaction between the framework designers and the use-case developers is necessary to find a layout, which is suitable for a generalized approach. Interfaces and APIs are described in detail together with the use-case providers to ensure the generalized approach and future sustainability of the framework as well as its easy application. It is ensured that use-case results are furthermore in line with the Design and Development of new Service prototypes task of PRACE 5IP/WP6. Suitable use-cases will be contributed to the PRACE GitLab Data Analytics project.

WP2 Deliverables – Status



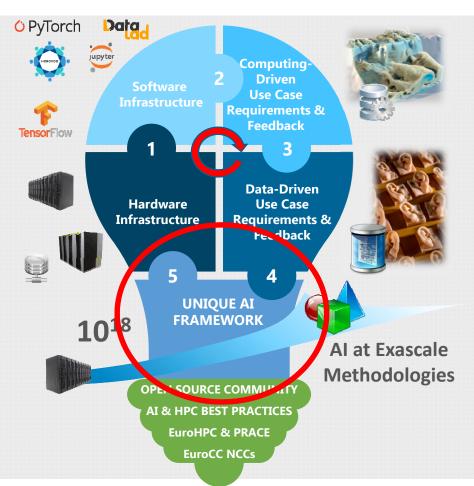
Del. No.	Delivery Name	Lead benificiary	Туре	Dis. Level	Del mo.	WP Leader	Responsible Editor, Authors	Internal Reviewers	Reviewers from PMT		Deadline subm. to EC	Submitted to EU on
D2.1	Best practice guidelines/tutorials for MSA/heterogenous systems	BSC	R	PU	2	M. Riedel/ UOI	G. Houzeaux/ BSC	S. Richard/ SAFRAN	A. Lintermann/ FZJ	08.02.2021	28.02.2021	26.02.2021
D2.5	Best practice guidelines/tutorials prototype	FZJ	R	PU	2	M. Riedel/ UOI	A. Lintermann/ FZJ	V. Khristenko/ CERN	J. Lopez/ ParTec	08.02.2021	28.02.2021	26.02.2021
D2.6	Support report	FZJ	R	PU	6	M. Riedel/ UOI	E. Inanc/ FZJ	R. Heylen/ FM	I. Schmitz/ ParTec	09.06.2021	30.06.2021	25.06.2021
D2.12	Software framework layout plan	UOI	OTHER	PU	9	M. Riedel/ UOI	M. Book/ UOI	R. Speck/ FZJ	J. Lopez/ ParTec	09.09.2021	30.09.2021	30.09.2021
D2.2	Report on porting & performance engineering	BSC	R	PU	12	M. Riedel/ UOI	G. Houzeaux/ BSC	M. Meinke/ RWTH	A. Lintermann/ FZJ	29.11.2021	31.12.2021	
D2.14	Report on novel Al technologies	UOI	R	СО	12	M. Riedel/ UOI	M. Riedel/ UOI	S. Kesselheim/ FZJ	J.Lopez/ ParTec	29.11.2021	31.12.2021	
D3.1	Report on outcomes of WP3 use-cases	RWTH	R	СО	12	W. Schröder/ RWTH	M. Meinke/ RWTH	S. Schlimpert/ FM	J.Lopez/ ParTec	29.11.2021	31.12.2021	
D4.1	Report on outcomes of WP4 use-cases	CERN	R	со	12	M.Girone/ CERN	E. Wulff/ CERN	H. Neukirchen/ UOI	I. Schmitz/ ParTec	29.11.2021	31.12.2021	

Talk of Task 2.4 (UOI): Software design of a unique AI framework includes interaction room process for co-design & more D2.12 details



Towards AI & HPC at Exascale with CoE RAISE Results





Hardware Infrastructure



Prepare & Document available production systems at partners' HPC centers

Examples: JUWELS (JUELICH), LUMI (UOICELAND), DEEP Modular Prototypes, JUNIQ (JUELICH), etc.

Software Infrastructure

Prepare & Document available open source tools & libraries for HPC & AI useful for implementing use cases Examples: DeepSpeed and/or Horovod for interconnecting N GPUs for a scalable deep learning jobs

Computing-driven Use Cases Requirements & Feedback

Use cases with emphasize on computing bring in co-design information about AI framework & hardware Examples: Use feedback that TensorFlow does not work nicely, so WP2 works with use cases on pyTorch

Data-driven Use Cases Requirements & Feedback

Use cases with emphasize on data bring in co-design information about AI framework & hardware Examples: Deployment blueprint by using AI training on cluster module & inference/testing on booster

→ UNIQUE AI FRAMEWORK

Living design document & software framework blueprint for using HPC & AI offering also pretrained AI models



Fact Sheet Process of CoE RAISE & Early Co-Design Examples

Modular

DEEP - DAM

Parallel

File

System

(Lustre)

Scalable

Storage

Service

Module

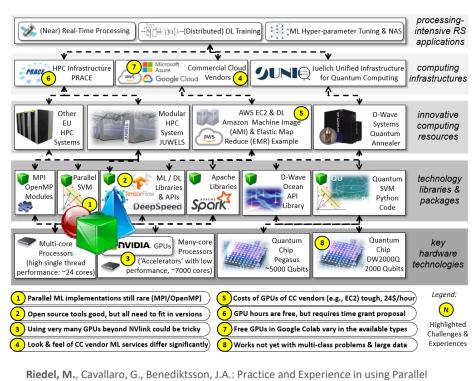
Modular

JUWELS

Network Attached

Memory





Riedel, M., Sedona, R., Barakat, C., Einarsson, P., Hassanian, R., Cavallaro, G., Book, M., Neukirchen, H., Lintermann, A.: Practice and Experience in using Parallel and Scalable Machine learning with Heterogenous Modular Supercomputing Architectures, in conference proceedings of the IEEE IDPDS Conference, Heterogenous Computing Workshop (HCW), Portland, USA, 2021, Online, to appear https://www.ipdps.org/



⋒ MPI & GIT-Dasc. Management Git-based Data JupyterLab OpenMl & Jupyter Training ML/DL Notebook Data DeepSpeed , Libraries Libraries

Singularity

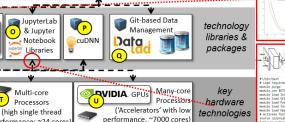
Container

Innovative

Memory

Hierarchies

ARDS Time Series Analysis



Canadian CBRAIN

Resource Execution

Neuroscience & BigBrain Research

Docker

Container

erformance: ~24 cores

HIBALL & Canadian

CBRAIN infrastructure

processing-

intensive

applications

computing

infrastructures

innovative

computing

resources

ARDS Time Series Analysis Covid-19 Chest X-Ray Analysis













and Scalable Machine learning in Remote Sensing from HPC over Cloud to Quantum Computing, in conference proceedings of the

IEEE IGARSS Conference, Brussels, Belgium, 2021, Physical and Online event, to appear

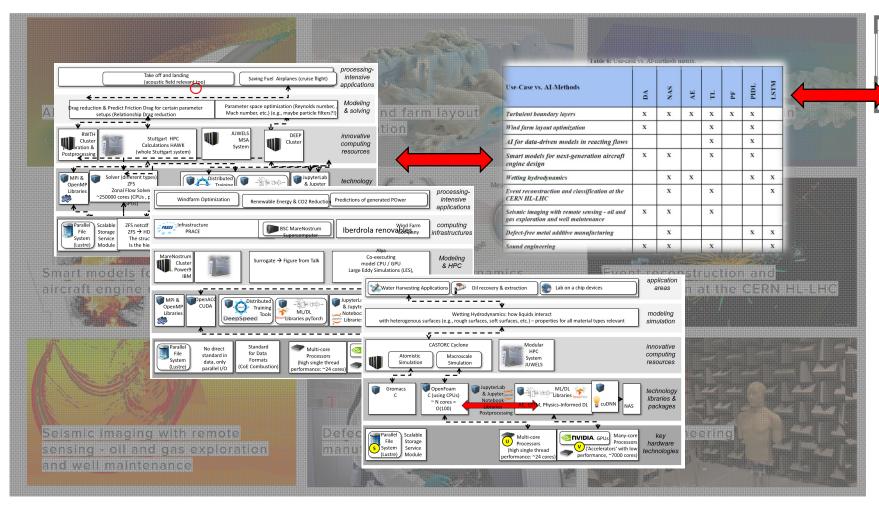
https://igarss2021.com/





WP2 - Fact Sheets in Close Collaboration with WP3/4





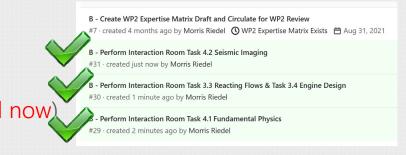


WORK IN PROGRESS

WP2 Fact Sheets Iteration – Next Steps: Interaction Rooms



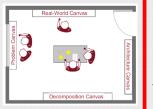
- > Follow-Through
 - ➤ Fact Sheet actions done → Closing
 - ▶ Interaction Rooms done → Closing (continue within tasks, plan another round now)
 - > Task-wise Interaction Rooms started

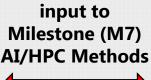






input to Interaction Room process







input to Deliverable D2.12 (M9) Layout plan AI Framework















 Use interaction room more often and have more meetings with WP3/WP4



B - Create Deliverable D2.12 - Software layout plan for a unique AI framework (M9) #33 · created 19 hours ago by Morris Riedel



B - Create Milestone M2 - AI/HPC Methods (M7)

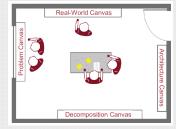
#32 · created 19 hours ago by Morris Riedel



HPC Systems Engineering in the Interaction Room Seminar



- > CoR RAISE Interaction Room Process as Next Step
 - Supports the proper software engineering design of the unique AI framework blueprint
 - Expecting to work with WP3& WP4 experts in an open minded way
 - Process will be guided by Prof. Dr. Matthias Book (University of Iceland)
 - Supported by Software Engineering & testing expert
 Prof. Dr. Helmut Neukirchen (University of Iceland)







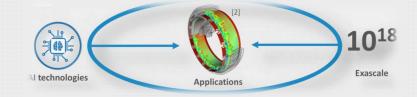
HPC Systems Engineering in the Interaction Room



Matthias Book

with Morris Riedel, Jülich Supercomputing Centre / Uol and Helmut Neukirchen, University of Iceland





- > CoE RAISE @ YouTube: https://www.youtube.com/channel/UCAdIZ-v6cWwGdapwYxdN7dg
- Methology as one CoE RAISE outcome

Book, M., Riedel, M., Neukirchen, H., Goetz, M.: Facilitating Collaboration in High-Performance Computing Projects with an Interaction Room, in conference proceedings of the 4th ACM SIGPLAN International Workshop on Software Engineering for Parallel Systems (SEPS 2017), October 22-27, 2017, Vancouver, Canada



Interaction Room Status & Discussions – WP3/WP4 Overview

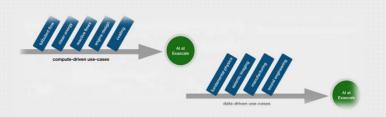


> WP3

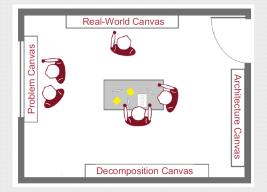
- > T3.1: Turbulent Flow (started)
- > T3.2: Clean Energy (started)
- > T3.3: Reactive Flows (started)
- > T3.4: Engine design (started)
- > T3.5: Coating (started)

>WP4

- > T4.1: Fundamental physics (started)
- > T4.2: Seismic imaging (started)
- > T4.3: Manufacturing (started)
- > T4.4: Sound engineering (started)





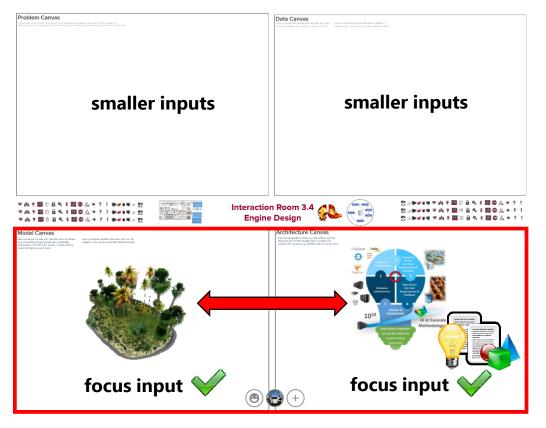


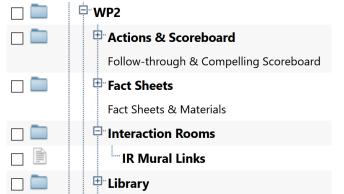
Use Case	CAE	PIML	ANNs	CNN		NO	SMs			GNN	IN	LSTM	GRU
Details			RBF- ANN	U-Net	RESNET	FNO	AR	ARMA	ARIMA		JEDI- net		
Al for turbulent boundary layers	х	х											
Al for wind farm layout optimization			х				х	х	х				
Al for data-driven models in reacting flows				×						х			
Smart models for next generation aircraft engine design				х						х			
Al for wetting hydrodynamics						х							
Event reconstruction and classification at the CERN HL- LHC use case										х	×		
Seismic imaging with remote sensing for energy applications	х				×								
Detect-free metal additive manufacturing	х				х								
Sound Engineering												×	х



Interaction Rooms via MURAL Boards & Refinements for D2.14 RASE









IR Mural Links

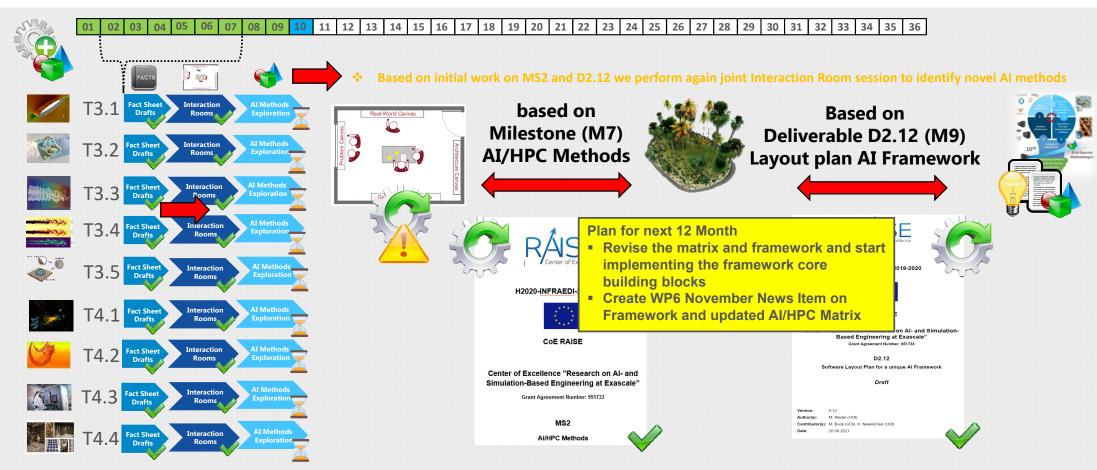
> TBD(Morris & use cases): another round of Mural board sessions





Compelling Scoreboard Status – Month 9



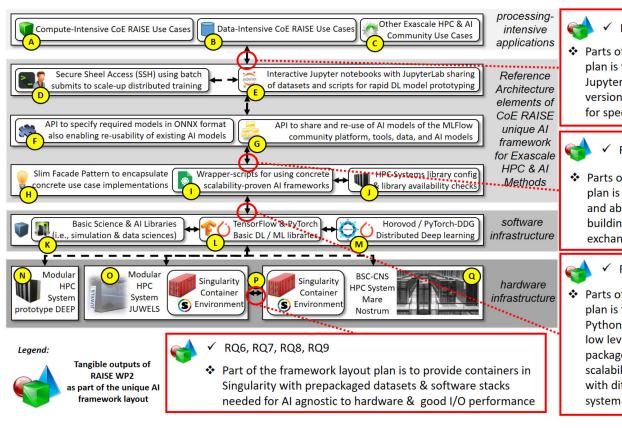


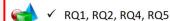


D2.12 Framework (M9) – Initial Blueprint for Discussions



> Available in BSCW: https://bscw.zam.kfa-juelich.de/bscw/bscw.cgi/3694045





Parts of the framework layout plan is to provide Kernels for Jupyter notebooks with correct version setups of modules for specific HPC Systems



Parts of the framework layout plan is to provide a lightweight and abstract Python API building on ONNX enabling also exchanges via MLFlow/ClearML



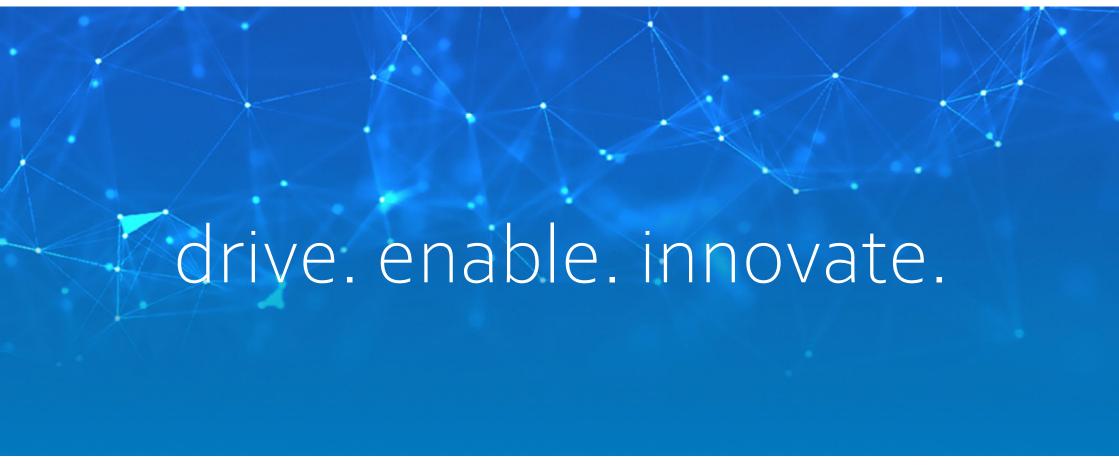
Parts of the framework layout plan is to provide a lightweight Python API that abstracts from low level versioning of AI packages (with proven scalability) and is harmonized with different available HPC system module versions





Continously Updating in another round of interaction rooms + adoption









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